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MAS-FIN-419

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 or facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.

By: Kingkong Chen

Date:

April 29, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.

: 10/723,906

Confirmation No. 1149

Applicant

: Wolfgang Hetzel et al.

Filed

: November 26, 2003

TC/A.U.

: 2811

Examiner

: Hung K. Vu

Title:

: Electronic Component Having Stacked

Semiconductor Chips in Parallel, and a Method

for Producing the Component

Docket No.

: MAS-FIN-419

Customer No. :

24131

BOX AF

Hon. Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE under 37 C.F.R. § 1.116

Sir:

In response to the <u>final</u> Office action dated March 25, 2005, please consider the following:

Remarks/Arguments begin on page 2 of this paper.